Sun4v WIP

- In HEAD - supports all devices
- Broken as of INTR_THREAD changes
- 2 serious issues block general use
  - aligned contiguous memory need by MD code
  - vm_alloc_contig requires Giant
  - Should be addressed with alc's contig-malloc import
- Floating point state is not always saved/restored correctly
As a result of Kris' and Jeff Roberson's recent work having 32-way machines to test on is much more interesting.

T2 (8 cores / 8 thread / core) based hardware will be available shortly.

Documentation for on-board 10GigE MACs currently unavailable.

In contrast to most of Sun - networking group not on the OSS bandwagon.
TOE WIP

- Poses some interesting API issues
  - Needs access to vlan tag for interfaces
  - Needs to keep host and card ARP cache in sync
- Should require no changes to TCP or IP code
- Initial support will be in a loadable module and will not be tightly coupled with the stack